



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

**2015 - 2016 VLSI IEEE FINAL YEAR Projects @ JP INFOTECH**

S.NO	Project Code	IEEE 2015-16 VLSI Project Titles	Domain	Lang/Year
1	JPV1501	40-Gb/s 0.7-V 2:1 MUX and 1:2 DEMUX with Transformer-Coupled Technique for SerDes Interface	VLSI	VLSI/2015
2	JPV1502	A 0.25-V 28-nW 58-dB Dynamic Range Asynchronous Delta Sigma Modulator in 130-nm Digital CMOS Process	VLSI	VLSI/2015
3	JPV1503	A 2.5-Gb/s DLL-Based Burst-Mode Clock and Data Recovery Circuit With 4× Oversampling	VLSI	VLSI/2015
4	JPV1504	A Class of SEC-DED-DAEC Codes Derived From Orthogonal Latin Square Codes	VLSI	VLSI/2015
5	JPV1505	A Combined SDC-SDF Architecture for Normal I/O Pipelined Radix-2 FFT	VLSI	VLSI/2015
6	JPV1506	A Dynamically Reconfigurable Multi-ASIP Architecture for Multistandard and Multimode Turbo Decoding	VLSI	VLSI/2015
7	JPV1507	A High-Performance FIR Filter Architecture for Fixed and Reconfigurable Applications	FIR Filter	VLSI/2015
8	JPV1508	A High-Throughput VLSI Architecture for Hard and Soft SC-FDMA MIMO Detectors	VLSI	VLSI/2015
9	JPV1509	A Low Power and High Sensing Margin Non-Volatile Full Adder Using Racetrack Memory	VLSI	VLSI/2015
10	JPV1510	A Low-Cost Hardware Architecture for Illumination Adjustment in Real-Time Applications	VLSI	VLSI/2015



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

11	JPV1511	A Low-Cost Low-Power All-Digital Spread-Spectrum Clock Generator	VLSI	VLSI/2015
12	JPV1512	A New Efficiency-Improvement Low-Ripple Charge-Pump Boost Converter Using Adaptive Slope Generator With Hysteresis Voltage Comparison Techniques	VLSI	VLSI/2015
13	JPV1513	A New Parallel VLSI Architecture for Real-time Electrical Capacitance Tomography	VLSI	VLSI/2015
14	JPV1514	A novel approach to realize Built-in-self-test(BIST) enabled UART using VHDL	VLSI	VLSI/2015
15	JPV1515	A Novel Area-Efficient VLSI Architecture for Recursion Computation in LTE Turbo Decoders	VLSI	VLSI/2015
16	JPV1516	A Novel Photosensitive Tunneling Transistor for Near-Infrared Sensing Applications: Design, Modeling, and Simulation	VLSI	VLSI/2015
17	JPV1517	A Relative Imaging CMOS Image Sensor for High Dynamic Range and High Frame-Rate Machine Vision Imaging Applications	VLSI	VLSI/2015
18	JPV1518	A Voltage Monitoring IC With HV Multiplexer and HV Transceiver for Battery Management Systems	VLSI	VLSI/2015
19	JPV1519	Accelerating Scalar Conversion for Koblitz Curve Cryptoprocessors on Hardware Platforms	VLSI	VLSI/2015
20	JPV1520	Aging-Aware Reliable Multiplier Design with Adaptive Hold Logic	VLSI	VLSI/2015
21	JPV1521	Algorithm and Architecture Design of the H.265/HEVC Intra Encoder	VLSI	VLSI/2015
22	JPV1522	All Digital Energy Sensing for Minimum Energy Tracking	VLSI	VLSI/2015
23	JPV1523	An Analytical Framework for Evaluating the Error Characteristics of Approximate Adders	VLSI	VLSI/2015



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

24	JPV1524	An Efficient Constant Multiplier Architecture Based on Vertical-Horizontal Binary Common Sub-expression Elimination Algorithm for Reconfigurable FIR Filter Synthesis	FIR Filter	VLSI/2015
25	JPV1525	An Efficient List Decoder Architecture for Polar Codes	VLSI	VLSI/2015
26	JPV1526	Analysis and Design of a Low-Voltage Low-Power Double-Tail Comparator	VLSI	VLSI/2015
27	JPV1527	Architecture for Monitoring SET Propagation in 16-bit Sklansky Adder	VLSI	VLSI/2015
28	JPV1528	Area-Efficient 3-Input Decimal Adders Using Simplified Carry and Sum Vectors	VLSI	VLSI/2015
29	JPV1529	Comparative Performance Analysis of the Dielectrically Modulated Full-Gate and Short-Gate Tunnel FET-Based Biosensors	VLSI	VLSI/2015
30	JPV1530	Design and Analysis of Inexact Floating-Point Adders	VLSI	VLSI/2015
31	JPV1531	Design Flow for Flip-Flop Grouping in Data-Driven Clock Gating	VLSI	VLSI/2015
32	JPV1532	Design of Efficient Content Addressable Memories in High-Performance FinFET Technology	VLSI	VLSI/2015
33	JPV1533	Design of Self-Timed Reconfigurable Controllers for Parallel Synchronization via Wagging	VLSI	VLSI/2015
34	JPV1534	Dual-Phase Tapped-Delay-Line Time-to-Digital Converter With On-the-Fly Calibration Implemented in 40 nm FPGA	VLSI	VLSI/2015
35	JPV1535	Energy Consumption of VLSI Decoders	VLSI	VLSI/2015
36	JPV1536	Exact and Approximate Algorithms for the Filter Design Optimization Problem	VLSI	VLSI/2015



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

37	JPV1537	Fast Code Design for Overloaded Code-Division Multiplexing Systems	VLSI	VLSI/2015
38	JPV1538	Fine-Grained Access Management in Reconfigurable Scan Networks	VLSI	VLSI/2015
39	JPV1539	FPGA-Based Bit Error Rate Performance Measurement of Wireless Systems	VLSI	VLSI/2015
40	JPV1540	Fully Pipelined Low-Cost and High-Quality Color Demosaicking VLSI Design for Real-Time Video Applications	VLSI	VLSI/2015
41	JPV1541	Fully Reused VLSI Architecture of FM0/Manchester Encoding Using SOLS Technique for DSRC Applications	VLSI	VLSI/2015
42	JPV1542	Graph-Based Transistor Network Generation Method for Supergate Design	VLSI	VLSI/2015
43	JPV1543	High Performance Low Swing Clock Tree Synthesis with Custom D Flip-Flop Design	VLSI	VLSI/2015
44	JPV1544	High-Speed and Energy-Efficient Carry Skip Adder Operating Under a Wide Range of Supply Voltage Levels	VLSI	VLSI/2015
45	JPV1545	High-Throughput LDPC-Decoder Architecture Using Efficient Comparison Techniques & Dynamic Multi-Frame Processing Schedule	VLSI	VLSI/2015
46	JPV1546	Implementation of Subthreshold Adiabatic Logic for Ultralow-Power Application	VLSI	VLSI/2015
47	JPV1547	In-Field Test for Permanent Faults in FIFO Buffers of NoC Routers	VLSI	VLSI/2015
48	JPV1548	Integrating Lock-Free and Combining Techniques for a Practical and Scalable FIFO Queue	VLSI	VLSI/2015
49	JPV1549	Learning Weighted Lower Linear Envelope Potentials in Binary	VLSI	VLSI/2015



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

		Markov Random Fields		
50	JPV1550	Level-Converting Retention Flip-Flop for Reducing Standby Power in ZigBee SoCs	VLSI	VLSI/2015
51	JPV1551	Long-Distance Measurement Applying Two High-Stability and Synchronous Wavelengths	VLSI	VLSI/2015
52	JPV1552	Low-Cost High-Performance VLSI Architecture for Montgomery Modular Multiplication	VLSI	VLSI/2015
53	JPV1553	MAC With Action-Dependent State Information at One Encoder	VLSI	VLSI/2015
54	JPV1554	Minimum Parallel Binary Adders with NOR (NAND) Gates	VLSI	VLSI/2015
55	JPV1555	Modified Wallace Tree Multiplier using Efficient Square Root Carry Select Adder	VLSI	VLSI/2015
56	JPV1556	Modulation Classification of Single-Input Multiple-Output Signals Using Asynchronous Sensors	VLSI	VLSI/2015
57	JPV1557	Novel Block-Formulation and Area-Delay-Efficient Reconfigurable Interpolation Filter Architecture for Multi-Standard SDR Applications	VLSI	VLSI/2015
58	JPV1558	Novel Design Algorithm for Low Complexity Programmable FIR Filters Based on Extended Double Base Number System	FIR Filter	VLSI/2015
59	JPV1559	Novel Reconfigurable Hardware Architecture for Polynomial Matrix Multiplications	VLSI	VLSI/2015
60	JPV1560	Obfuscating DSP Circuits via High-Level Transformations	VLSI	VLSI/2015
61	JPV1561	One Minimum Only Trellis Decoder for Non-Binary Low-Density Parity-Check Codes	VLSI	VLSI/2015



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry

Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

62	JPV1562	Partially Parallel Encoder Architecture for Long Polar Codes	VLSI	VLSI/2015
63	JPV1563	Pre-Encoded Multipliers Based on Non-Redundant Radix-4 Signed-Digit Encoding	VLSI	VLSI/2015
64	JPV1564	Range Unlimited Delay-Interleaving and –Recycling Clock Skew Compensation and Duty-Cycle Correction Circuit	VLSI	VLSI/2015
65	JPV1565	Recursive Approach to the Design of a Parallel Self-Timed Adder	VLSI	VLSI/2015
66	JPV1566	Reverse Converter Design via Parallel-Prefix Adders: Novel Components, Methodology, and Implementations	VLSI	VLSI/2015
67	JPV1567	Revisiting Central Limit Theorem: Accurate Gaussian Random Number Generation in VLSI	VLSI	VLSI/2015
68	JPV1568	Shift Register Design Using Two Bit Flip-Flop	VLSI	VLSI/2015
69	JPV1569	Signal Design for Multiple Antenna Systems With Spatial Multiplexing and Noncoherent Reception	VLSI	VLSI/2015
70	JPV1570	Synthesis of Genetic Clock with Combinational Biologic Circuits	VLSI	VLSI/2015
71	JPV1571	Timing Error Tolerance in Small Core Designs for SoC Applications	VLSI	VLSI/2015
72	JPV1572	Two-Step Optimization Approach for the Design of Multiplierless Linear-Phase FIR Filters	FIR Filter	VLSI/2015
73	JPV1573	VLSI-Assisted Non-rigid Registration Using Modified Demons Algorithm	VLSI	VLSI/2015

More titles will be updated soon. For updated titles and other details visit: [www.jpinfotech.org](http://www.jpinfotech.org) or  
[www.jpinfotech.blogspot.com](http://www.jpinfotech.blogspot.com)



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

### PROJECT SUPPORT TO REGISTERED STUDENTS:

- 1) IEEE Base paper.
- 2) Abstract Document.
- 3) Future Enhancement (based on Requirement).
- 4) Modified Title / Modified Abstract (based on Requirement).
- 5) Complete Source Code.
- 6) How to Run execution help file.
- 7) Software Packages
- 8) International Conference / International Journal Publication based on your project.

### **OUR OTHER SALIENT FEATURES:**

- Number 1 Project Master in Puducherry/Chennai.
- Successfully conducted more than 15 International Conferences in all over South India in the year 2013 & 2014.
- For the year 2015- 2016, we have Signed MoU with 15 Engineering Colleges in all over South India to Conduct International Conferences in year 2015 – 2016.



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

- Published more than 1000 Research Articles of Our Ph.D./M.Phil/ME/M.Tech./BE/B.Tech. Students in Leading International Conferences and International Journals for the year 2014-2015.
- For the year 2013- 2014 Recognized and Awarded from “PONDICHERRY UNIVERSITY”, “Paavai College of Engg”, “Arjun College of Technology” and many leading institutions.
- For the year 2014-2015 Recognized and Awarded from the following colleges:
  - “V.P.Muthaiah Pillai Meenakshi Ammal Engineering College for Women”
  - “Muthayammal Arts and Science College”
  - “Sri Raaja Raajan College of Engineering and Technology”
  - “Latha Mathavan Engineering College”
  - “SKR engineering college”
  - “Dr Pauls Engineering College”
  - “Jaya Engineering College”
  - “Jain College of Engineering”
- Recognized and published article about JP INFOTECH and our director in “THE HINDU”, “DINAKARAN” and many more newspapers and Media.
- Leaders with more than 7+ years of experience
- Guided more than 15,000 students.



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690





**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

Chennai Office : JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

Landmark : Next to Kotak Mahendra Bank.

Pondicherry Office : JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry  
Landmark : Next to VVP Nagar Arch.

[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)

- We assist and guarantee you to publish a paper on your project in INTERNATIONAL JOURNAL PUBLICATIONS / INTERNATIONAL CONFERENCE PUBLICATIONS.
- NO FALSE PROMISES
- 100% Assurance for Project Execution
- 100% LIVE EXPLANATION
- VALID TRAINEE CERTIFICATION (ISO Certification)
- 100% PLACEMENT SUPPORT
- Own Projects are also welcomed.

So don't wait any more!!! Join us and be a part of us. Walk-in to our Office OR E-mail us your requirements and Register your projects.

For any queries Contact:

**R.JAYAPRAKASH** BE,MBA,M.Tech., (Ph.D.)

Managing Director

[www.jpinfotech.blogspot.com](http://www.jpinfotech.blogspot.com)

[www.jpinfotech.org](http://www.jpinfotech.org)

FOLLOW

US

ON

FACEBOOK

@

<https://www.facebook.com/jpinfotechresearch>



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE,MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

[www.jpinfotech.org](http://www.jpinfotech.org)

(0)9952649690

**Chennai Office :** JP INFOTECH, Old No.31,  
New No. 86, 1st Floor , 1st Avenue , Ashok  
Pillar , Chennai - 83.

**Landmark :** Next to Kotak Mahendra Bank.

**Pondicherry Office :** JP INFOTECH , #45 ,  
Kamaraj Salai, Thattanchavady, Puducherry

**Landmark :** Next to VVP Nagar Arch.

**[jpinfotechprojects@gmail.com](mailto:jpinfotechprojects@gmail.com)**

**MOBILE: (0)9952649690**

**Chennai Office:** JP INFOTECH, Old No.31, New No.86, 1<sup>st</sup> Floor, 1<sup>st</sup> Avenue,  
Ashok Pillar, Chennai – 83. Landmark: Next to Kotak Mahendra Bank / Bharath  
Scans.

Landline: (044) - 43012642 / Mobile: (0)9952649690

**Pondicherry Office:** JP INFOTECH, #45, Kamaraj Salai, Thattanchavady,  
Puducherry – 9. Landmark: Opp. To Thattanchavady Industrial Estate & Next to  
VVP Nagar Arch.

Landline: (0413) - 4300535 / Mobile: (0)8608600246 / (0)9952649690



**JP INFOTECH**

SOFTWARE DEVELOPMENT & RESEARCH DIVISION

Contact: R.JAYAPRAKASH BE.MBA,M.Tech.(Ph.D.), Mobile: (0)9952649690